

Abstracts

New methodology for microwave/millimeter-wave MMIC development

K. Kamogawa, K. Nishikawa, I. Toyoda, M. Tokumitsu, M. Hirano, T. Nakagawa and M. Muraguchi. "New methodology for microwave/millimeter-wave MMIC development." 2000 MTT-S International Microwave Symposium Digest 00.3 (2000 Vol. III [MWSYM]): 1913-1916.

A new MMIC development methodology that combines suitable devices with the 3D/multilayer interconnection process is presented. This approach allows the designer to choose the process so as to realize high performance and low cost. The integration of 0.15-/spl mu/m pHEMT and 3D interconnection technologies is demonstrated. The slight changes of FET parameter with polyimide layer use can be easily incorporated into MMIC design and development.

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